

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAIL STOP PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE UNDER 37 CFR 1.10

I hereby certify that this correspondence and the documents referred to as attached therein are being deposited on July 14, 2003 with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee," mailing label No. EV 349852506 US addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

7/14/03

Date

Keith M. Tackett

Signature

21910 U.S. PRO  
10/619193



07/14/03

Re: Inventor(s): ITO, Hiroyuki  
Title: Ion Implantation Method and Method for Manufacturing SOI Wafer

Transmitted herewith is the patent application identified above, including:

- Specification, claims and abstract 26 Total Pages
- Drawings  Formal  Informal 4 Total Pages
- Executed Declaration and Power of Attorney
- Information Disclosure Statement with List
- Assignment of the Invention to
- Assignment Recordation Cover Sheet

FEE CALCULATION (37 CFR 1.16)

	NUMBER OF CLAIMS FILED	LESS NUMBER PAID BY BASIC FEE	NUMBER OF EXTRA CLAIMS (Not less than zero)	LARGE ENTITY FEE
Basic Fee				\$750.00
Total Claims	9	- 20 =	X \$18 =	\$0.00
Independent Claims	1	- 3 =	X \$84 =	\$0.00
First Presentation of Multiple Dependent Claims			+ \$.	-0-
				\$750.00
Total Filing Fee Calculation				

- The Commissioner is hereby authorized to charge \$750.00 to Deposit Account No. 20-0782 SOEI/0056. **A duplicate copy of this transmittal is enclosed.**
- The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 20-0782/SOEI/0056. **A duplicate copy of this transmittal is enclosed.**
- Please address all future correspondence to:  
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Respectfully submitted,

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